# Code Generation: Sethi Ullman Algorithm 

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- Extensions to take into account algebraic properties of operators.
- Generates optimal code - i.e. code with least number of instructions. There may be other notions of optimality.
- Complexity is linear in the size of the expression tree.

Reasonably efficient.

## Expression Trees

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- We have not identified common sub-expressions; else we would have a directed acyclic graph (DAG):



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- In this example
$\Sigma=\{a, b, c, d, e, \ldots\}$, and $\Theta=\{+,-, *, /, \ldots\}$


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4. }\mp@subsup{r}{2}{}\leftarrow\mp@subsup{r}{2}{}\mathrm{ op }\mp@subsup{r}{1}{}\mathrm{ (the result of }\mp@subsup{r}{2}{}\mathrm{ op }\mp@subsup{r}{1}{}\mathrm{ is stored in r}\mp@subsup{r}{2}{}\mathrm{ )
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- Note:


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- Note:

1. In instruction 3, the memory location is the right operand.
2. In instruction 4, the destination register is the same as the left operand register.

## Key Idea

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- If the left and right subtrees require $I_{1}$, and $I_{2}\left(I_{1}<I_{2}\right)$ registers respectively, what should be the order of evaluation?


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- The maximum register requirement in this case is

$$
\max \left(I_{1}, I_{2}+1\right)=I_{2}+1
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Therefore the subtree requiring more registers should be evaluated first.

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- Left and the right leaves are labeled 1 and 0 respectively, because the left leaf must necessarily be in a register, whereas the right leaf can reside in memory.


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- Visit the tree in post-order. For every node visited do:

1. Label each left leaf by 1 and each right leaf by 0 .
2. If the labels of the children of a node $n$ are $I_{1}$ and $I_{2}$ respectively, then

$$
\begin{aligned}
\operatorname{label}(n) & =\max \left(I_{1}, l_{2}\right), \text { if } I_{1} \neq I_{2} \\
& =I_{1}+1, \text { otherwise }
\end{aligned}
$$

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3. gencode( $n$ ) evaluates $n$ in the register on the top of the stack.
4. Temporary allocation is done from a stack of temporary names tstack, initially containing $t_{0}, t_{1}, \ldots, t_{k}$ (with $t_{0}$ on top of the stack).

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3. gencode( $n$ ) evaluates $n$ in the register on the top of the stack.
4. Temporary allocation is done from a stack of temporary names tstack, initially containing $t_{0}, t_{1}, \ldots, t_{k}$ (with $t_{0}$ on top of the stack).
5. swap(rstack) swaps the top two registers on the stack.

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1. $n$ is a left leaf:


$$
\operatorname{gen}(\operatorname{top}(r s t a c k) \leftarrow \text { name })
$$

Comments: $n$ is named by a variable say name. Code is generated to load name into a register.

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\begin{aligned}
& \operatorname{gencode}\left(n_{1}\right) \\
& \operatorname{gen}(\operatorname{top}(r s t a c k) \leftarrow \text { top }(r s t a c k) \text { op name })
\end{aligned}
$$

Comments: $n_{1}$ is first evaluated in the register on the top of the stack, followed by the operation op leaving the result in the same register.

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swap(rstack);
gencode( $n_{2}$ );

Right child goes into next to top register Evaluate right child

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swap(rstack);
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push(rstack, R);
swap(rstack) Restore register stack

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gencode $\left(n_{1}\right)$;
$R:=\operatorname{pop}(r s t a c k) ;$
gencode( $n_{2}$ );
$\operatorname{gen}(R \leftarrow R$ op top $(r s t a c k))$;
push(rstack, $R$ )
Comments: Same as case 3, except that the left sub-tree is evaluated first.

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5. Both the children of $n$ require registers greater or equal to the available number of registers.


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Comments: In this case the right sub-tree is first evaluated into a temporary. This is followed by the evaluations of the left sub-tree and $n$ into the register on the top of the stack.

## An Example

For the example:


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assuming two available registers $r_{0}$ and $r_{1}$, the calls to gencode and the generated code are shown on the next slide.

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- We shall now elaborate on each of these.


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2. Each node of the expression tree is visited exactly once. If this node specifies a binary operation, then the algorithm branches into steps $2,3,4$ or 5 , and at each of these places code is generated to perform this operation exactly once.

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3. The number of stores is optimal: this is harder to show.

- Define a major node as a node, each of whose children has a label at least equal to the number of available registers.
- If we can show that the number of stores required by any program computing an expression tree is at least equal the number of major nodes, then our algorithm produces minimal number of stores (Why?)


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- Assume that the tree has $M$ major nodes.
- Now consider a tree formed by replacing the subtree $S$ evaluated by the first store, with a leaf labeled by a name $I$.

- Let $n$ be the major node in the original tree, just above $S$, and $n_{1}$ and $n_{2}$ be its immediate descendants ( $n_{1}$ could be $/$ itself).


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4. Therefore the number of major nodes in the modified tree is $M-1$.

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3. The node $n$ may no longer be a major node but all other major nodes in the original tree continue to be major nodes in the modified tree.
4. Therefore the number of major nodes in the modified tree is $M-1$.
5. If we assume as induction hypothesis that the number of stores for the modified tree is at least $M-1$, then the number of stores for the original tree is at least $M$.

## SETHI-ULLMAN ALGORITHM: COMPLEXITY

Since the algorithm visits every node of the expression tree twice once during labeling, and once during code generation, the complexity of the algorithm is $O(n)$.

